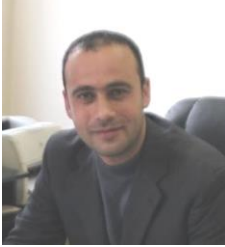


# Bashar Musa

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## Personal Information



Name	Bashar F.K Musa
Date of Birth	18/8/1979
Place of Birth	Jenin/ Palestine
Status	Married
Citizenship	Palestinian
Address	Palestinian– West Bank – Jenin /Merkeh

## Education

- 2007 - 2011 Al-Quds University  
Jerusalem, Palestine  
***M.Sc. Electronic & Computer Engineering (81%)***
- 1997 - 2002 Birzeit University  
Ramallah, Palestine  
***B.Sc. Electrical Engineering, Attention in communications and computer fields. (Excellent Evaluation)***
- 1996 - 1997 Arrabeh Secondary School  
Arrabeh, Jenin, Palestine  
***Tawjeehi (90%)***

## Master Thesis

***Quality of Service Improvement for WiMAX Physical Layer***

## Educational Background

- Wireless Communication Systems.
- Digital Signal Processing.
- Microprocessor Systems & Applications.
- Power Systems.
- Control Systems.
- Computer Networks.

## Employment

- **November 2011 – Until now :**  
Lecturer in Telecommunication Engineering Department  
Arab American University-Jenin
- **February 2003 – September 2011:**  
Teaching & Research Assistant  
Telecommunication Engineering Department  
Arab American University-Jenin

## Teaching Experience

- Electrical Circuits
- Measurements and Instrumentation
- Analog Communications
- Modeling and Simulation of Communication Systems
- Signals and Systems
- Electrical Circuits Lab
- Electronics Lab
- Semiconductors Lab
- Analog Communications Lab
- Digital Communications Lab
- Digital Logic Design Lab
- Digital Signal Processing Lab
- Antenna & Microwave lab

## Training Experience

### **Siemens company**

Ramallah - Palestine, Aug. 2001

Training Period: Seven Weeks.

Description: software and hardware design of telecommunication boards (switches, exchangers). Programming different types of logic devices specially FPGA's and PLD's. Installing software to the designed boards using Visual C, C++. Studying modern telecommunication technologies as SDH, PDH, ATM, TCP/IP, and others

## Training Courses

- **Java Development Certified Professional - (80 hours)**  
Arab American University, June 2003
- **Matlab Simulation – (40 hours)**  
Palestine Polytechnic University , June 2009

- **Developing Methods for Co-design with FPGA – (40 hours)**  
An Najah National University, Jan 2010

## **Computer Skills**

- MATLAB.
- C++ language programming.
- Verilog "Hardware Description language".
- Workbench multisim
- Orcade Design.
- PSpice.
- Assembly language.
- PLC programming.
- AutoCAD Drawing.

## **Languages**

- Arabic: Mother language.
- Fluency in English (writing, reading, speaking)

## **Projects List**

- Final graduation project (Modeling and Simulink of GSM System and it's Future Trends) which include a survey of the GSM networks, Interfaces, Modulation, Traffic Cases, Services, Comparison between GSM and CDMA (IS-95a). And make Simulation for both systems (GSM & IS-95) using ACOLADE and MATLAB packages to show the systems capacity, noise and fading effects.
- Design of Ethernet MAC Using Verilog Programming. (Software design)
- Project in hardware design of Ultrasonic Transmitter & Receiver.